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Approved For Release 2002/06/17 : CIA-RDP78B04747A001500080090-4

DRAFT

IAS/TA - D R A F T  
15 September 1967

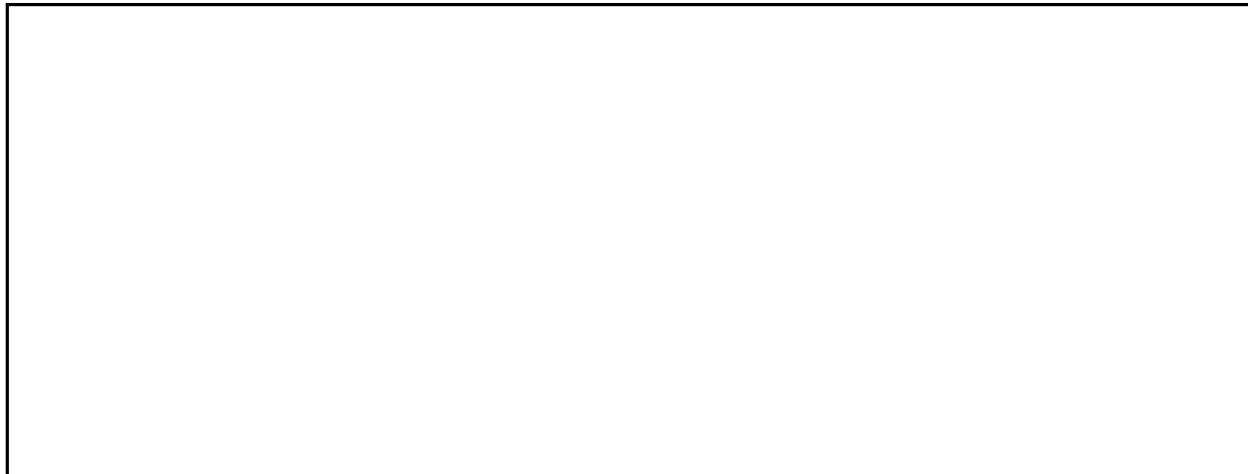
MEMORANDUM FOR: Assistant for Technical Development, NPIC

SUBJECT: Coordination of Chip Comparator System

25X1A 1. Through your memo of 30 August 1967 and the subsequent meeting  
25X1A with [ ] representatives on 7 September we are aware of  
25X1A the history, TDS actions, and current status of the Chip Comparators  
planned for use in [ ]

We understand that [ ], as contract monitor, will be responsible for final acceptance of the 405-B Chip Comparators, and that he will rely on technical assistance from [ ] and his EPS personnel. We have, in fact, had preliminary discussions with [ ] regarding our planned daily check-out procedures for the 405-AM and his weekly or bi-weekly calibration tests.

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25X1A 4. We feel that since the ultimate effective use of as many as 6 Chip Comparators in [ ] depends on considerable coordination between the users (IAS, PAG, TID, and DIAAP-9); LPD (for computer service); and TDS/EPS (for preventive maintenance), it is important that a CHIP COMPARATOR SYSTEM COORDINATOR be named at an early date. Our definition of the SYSTEM includes not only the hardware, i.e., comparator, plotter, etc., but also the necessary software including computer programs, operator worksheets (for ephemeral data), operator techniques, daily performance tests, preventive maintenance procedures, "real-time" requirements, future programming requirements, etc. This responsibility, we believe, transcends the user and service components and should rest

DECLASS REVIEW by NIMA/DOD

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GROUP 1  
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downgrading and  
declassification

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with the Dev. Br./TDS. To our knowledge, [ ] served this function during the early stages of the Chip Comparison program, but now is apparently concerned with the Chip Comparison process, and not the SYSTEM as a whole.

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5. If you agree with the above concept, we suggest a meeting, with appropriate component representatives in attendance, at an early date to discuss problems of mutual concern. We would be pleased to participate, and can offer a measure of assistance based on experiences gained on the 405-AM comparator.

[ ]  
Director  
Imagery Analysis Service

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*File Programming General*  
NPIC/TDS/P-321-67  
29 September 1967

MEMORANDUM FOR: Assistant for Technical Development, NPIC  
SUBJECT: Coordination of Chip Comparator System  
REFERENCE: IAS/TA-DRAFT, Same Subject, Dated 15 September 1967

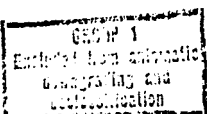
1. The model 405B Chip Comparator coordination problem is really one of a sub-system to the overall exploitation problem. To be more specific, while the Chip Comparator as a system must interface directly with the IIS and the Chip Concept (including printers and processors), it is also forerunner of future developments in Image Analysis (improved chip quality), Materials Handling, Information Handling, and the High Precision Stereo Comparator.

2. I recommend that we handle the specific problems outlined in the referenced document without attempting to broaden the scope into a full Systems Project Office (SPO) operation at this time.

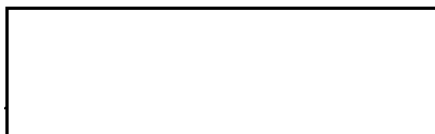
3. The type of activity suggested by IAS is actually one of a combination of Acceptance Testing, Service Testing and Operational Suitability Testing of a series of 405-B Chip Comparators (with their supporting techniques and equipments) in relation to the 405-AM Chip Comparator which it is either supplementing or replacing. I realize that our Equipment Performance Staff is not up to T.O. or expertise required from a manpower standpoint, but functionally this activity should be assigned to an EPS.

4. I am very pleased to see the system approach applied, but some additions are needed to Par. 4 of the referenced memorandum. Specifically:

- a. TDS/DS should be involved for technical assistance and feedback to related current and future developments.
- b. SS/TB should be involved in training activities.
- c. SS/LB should be involved in facilities and logistics interface.



5. I recommend that a Project Officer be designated by NPIC management to head an ad hoc task team for "Test and Integration of the 405-B/405-AM/IIS Program." The Project Officer should be given the responsibility and authority to act for NPIC rather than the component to which he is assigned. Mr. West is a logical candidate for such a function.



Lt. Col., USAF  
Chief, Technical Planning Staff, TDS, NPIC

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NPIC/TDS-180/67  
11 October 1967

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MEMORANDUM FOR: Imagery Analysis Staff

ATTENTION : 

SUBJECT : Coordination of Chip Comparator System

REFERENCE : IAS/TA-DRAFT, Same Subject, Dated 15 September 1967

1. We also view the Chip Comparator coordination problem as one of a sub-system to the overall exploitation problem. To be more specific, while the Chip Comparator as a system must interface directly with the IIS and the Chip Concept (including printers and processors), it is also forerunner of future developments in Image Analysis (improved chip Quality), Materials Handling, Information Handling, and the High Precision Stereo Comparator. However, our current problem remains to be the operational potential, assuming the 405B will function as well as the 405B/AM has for the past seven weeks and that the funding problems with  can be resolved. The most serious problem facing the users (IAS, PAG and TID) is the lack of available software and computer time.

2. We concur with your suggested meeting and the naming of an NPIC project officer with the responsibility and authority to act for NPIC rather than the component to which he is assigned.

3. I was pleased with your system approach, but some additions will be needed in paragraph 4. Specifically:

a. TDS/DS should be involved for technical assistance and feedback to related current and future developments.

b. SS/TB should be involved for training activities.

c. SS/LB should be involved for facilities and logistics interface.

4. I have our Equipment Performance Staff alerted to perform the Acceptance, Service and Operational Suitability Testing on the Comparators.

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SUBJECT: Coordination of Chip Comparator System

5. I was pleased to have the opportunity of reviewing your draft memorandum. We are glad for your interest and support.

25X1A

Assistant for Technical Development, NPIC

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